Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

1. (currently amended) A method for managing ~~storing~~ pixel image data, the method comprising:

retrieving the pixel image data comprising a plurality of colorspace components ~~from a first memory~~, wherein ~~a pixel comprises individual~~ each colorspace components is one of three different types~~, and wherein each burst comprises colorspace components of a single type~~;

storing the plurality of colorspace components in one continuous machine-readable memory segment in a machine-readable memory, the machine readable memory having a plurality of bursts and one or more burst boundaries, wherein one type of colorspace component is stored in each burst; and

copying the pixel image data at least in part by accessing the plurality of colorspace components from the machine-readable memory. ~~to a video frame being decoded for display.~~

2. (original) The method of claim 1 wherein the machine-readable memory comprises volatile memory.

3. (original) The method of claim 2 wherein the volatile memory comprises dynamic random access memory.

4. (original) The method of claim 2 wherein the volatile memory comprises static random access memory.

5. (original) The method of claim 1 wherein the colorspace components comprise luminance, red difference sample, and blue difference sample.

6. (original) The method of claim 1 wherein the colorspace components comprise a red color level, a green color level, and a blue color level.

7. (original) The method of claim 1 wherein the pixel image data comprises a first data byte, the first data byte being registered at a memory address immediately following one of the one or more burst boundaries.

8. (original) The method of claim 1 wherein the pixel image data comprises a first data byte and subsequent data bytes, one of the subsequent data bytes being registered at a memory address immediately following one of the one or more burst boundaries.

9-16. (cancelled)

17. (new) A processing device for managing pixel image data in a plurality of memory bursts, the processing device comprising:

a first circuit for retrieving and storing the pixel image data comprising a plurality of colorspace components, wherein each colorspace component is one of three different types, and wherein one type of colorspace component is stored in each memory burst; and

a second circuit for copying the plurality of colorspace components from the memory bursts.

18. (new) The processing device of claim 17 wherein the plurality of memory bursts are in volatile memory.

19. (new) The processing device of claim 18 wherein the volatile memory comprises dynamic random access memory.

20. (new) The processing device of claim 18 wherein the volatile memory comprises static random access memory.

21. (new) The processing device of claim 17 wherein the colorspace components comprise luminance, red difference sample, and blue difference sample.

22. (new) The processing device of claim 17 wherein the colorspace components comprise a red color level, a green color level, and a blue color level.

23. (new) The processing device of claim 17 wherein the pixel image data comprises a first data byte, the first data byte being registered at a memory address immediately following a boundary of a memory burst in the plurality of memory bursts.

24. (new) The processing device of claim 17 wherein the pixel image data comprises a first data byte and subsequent data bytes, one of the subsequent data bytes being registered at a memory address immediately following a boundary of a memory burst in the plurality of memory bursts.